

FORM PTO-1449

## INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.  
1778.0200000APPLICATION NO.  
09/836,541FIRST NAMED INVENTOR  
Ryan C. KinterFILING DATE  
April 18, 2001ART UNIT  
2183

## U.S. PATENT DOCUMENTS

| EXAMINER<br>INITIAL |     | DOCUMENT<br>NUMBER | DATE | NAME | CLASS | SUB-<br>CLASS | FILING DATE |
|---------------------|-----|--------------------|------|------|-------|---------------|-------------|
|                     | AA1 |                    |      |      |       |               |             |
|                     | AB1 |                    |      |      |       |               |             |
|                     | AC1 |                    |      |      |       |               |             |
|                     | AD1 |                    |      |      |       |               |             |
|                     | AE1 |                    |      |      |       |               |             |
|                     | AF1 |                    |      |      |       |               |             |
|                     | AG1 |                    |      |      |       |               |             |
|                     | AH1 |                    |      |      |       |               |             |
|                     | AI1 |                    |      |      |       |               |             |
|                     | AJ1 |                    |      |      |       |               |             |
|                     | AK1 |                    |      |      |       |               |             |

## FOREIGN PATENT DOCUMENTS

| EXAMINER<br>INITIAL |     | DOCUMENT<br>NUMBER | DATE    | COUNTRY | CLASS | SUB-<br>CLASS | TRANSLATION |
|---------------------|-----|--------------------|---------|---------|-------|---------------|-------------|
| /D.P./              | AL1 | 0 109 567 A2       | 05/1984 | EP      |       |               | N/A         |
| /D.P./              | AM1 | 0 170 398 A2       | 02/1986 | EP      |       |               | N/A         |
| /D.P./              | AN1 | WO 95/30187 A1     | 11/1995 | WO      |       |               | N/A         |
|                     | AO1 |                    |         |         |       |               | Yes<br>No   |
|                     | AP1 |                    |         |         |       |               | Yes<br>No   |

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

|        |    |   |  |
|--------|----|---|--|
| /D.P./ | AR | 1 | Hirata, H., <i>et al.</i> , "An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads," ACM SIGARCH Computer Architecture News, Volume 20, Number 2, pgs. 136-145, Association for Computing Machinery (May 1992). |
|        | AS | 1 |  |
|        | AT | 1 |  |

EXAMINER /Daniel Pan/

DATE CONSIDERED  
06/13/2008

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.